

ABSTRACT OF THE DISCLOSURE

5 A packet processing apparatus that realizes improved overall relaying performance by distributing input packets to multiple packet analyzing modules for information processing is disclosed. The packet processing apparatus includes a distributor for assigning a sequence number to each of the input packets and distributing the numbered packets. The packet analyzing modules realize parallel execution of information analyzing processes on the packets distributed from the distributor. An order correction buffer rearranges the packets supplied from the packet analyzing modules in order according to the sequence numbers assigned to the packets and outputs the packets in the rearranged order.

10